

**IN THE CLAIMS:**

**Please amend the claims as follows:**

1-14 (cancelled).

15 (currently amended). A liquid crystal on silicon imaging device, comprising:

a cover glass;

a silicon backplane physically connected to the cover glass in a connection area, the connection area defined by a generally rectangular adhesive strip; and

a liquid crystal sealed between said cover glass and said silicon backplane by the adhesive strip;

wherein said silicon backplane comprises:

a frame buffer configured to store pixel data located at least partially under the adhesive strip;

a pixel array located completely within the connection area;

an interface control block connected between the frame buffer

and the pixel array, the interface control block being adapted to

determine pulse width modulation waveforms for the pixel array in accordance with the pixel data stored in the frame buffer;

an external interface block data, located at least partially under

the adhesive strip, configured to provide an external interface to the device, including receiving pixel data and transferring the received pixel data into the frame buffer; and  
a control block data, located at least partially under the adhesive strip, connected to the external interface block, the frame buffer, and the interface control block, the control circuit being adapted to provide control signals to operate the device,

wherein at least a portion of the frame buffer block includes memory cells co-located with pixel-elements of the pixel array.

16 (cancelled).

17 (original). The liquid crystal on silicon imaging device as recited in claim 15, wherein the frame buffer includes a front buffer and a back buffer.

18 (cancelled).

19 (previously presented). The liquid crystal on silicon imaging device as recited in claim 15, wherein the frame buffer, the interface control block, and the pixel array are divided into first and second parts, wherein the first part is associated with a first half of rows of the pixel array and the second part is associated with a second half of rows of the pixel array.

P15059D

Serial No. 10/808,990

20-24 (cancelled).